

END SEMESTER EXAMINATION TIME TABLE THEORY EXAMINATION MAY-2025

M.Tech.

**(Civil Engineering/Computer Science and Engineering/ Electronics Engineering/Mechanical Engineering)
(Even Semester)**

Branch	Specializations	Date	Sem.	10.05.2025	13.05.2025	15.05.2025	17.05.2025	24.05.2025	28.05.2025
		Day		Saturday	Tuesday	Thursday	Saturday	Saturday	Wednesday
		Exam Time							
Civil Engineering	Structural Engineering	01:30 PM - 04:30 PM	2nd	CE5CS01 (T) Theory of Elasticity	CE5EL01 (T) Advance Concrete Technology	CE5CS06 (T) Design of Concrete Structures	-	-	-
Computer Science and Engineering	Artificial Intelligence	09:30 AM- 12:30 PM	4th	-	-	-	-	CS5AI06 (T) Digital Image Processing	EN5HS01 (T) Entrepreneurship and Management
	Artificial Intelligence	01:30 PM - 04:30 PM	2nd	CS5EL24 (T) Data Science	CS5EL22 (T) Neural Networks	CS5AI05 (T) Natural Language Processing	-	-	-
Electronics Engineering	VLSI	01:30 PM - 04:30 PM	2nd	EC5EL21 (T) Design for Testability	EC5EL22 (T) Low Power VLSI Design	EC5CV02 (T) FPGA Architecture & Application	-	-	-
Mechanical Engineering	-	09:30 AM- 12:30 PM	4th	-	-	-	-	ME5CD06 (T) Design of Dynamic Systems	EN5HS01 (T) Entrepreneurship and Management
Mechanical Engineering	-	09:30 AM- 12:30 PM	Backlog	-	-	-	ME5BS01 (T) Mathematics	-	-
Mechanical Engineering	-	01:30 PM - 04:30 PM	2nd	ME5EL41 (T) Fluid Flow & Heat Transfer in IC Engines	ME5CA01 (T) Finite Element Method	ME5EL42 (T) Alternative Fuels & Technology	-	-	-

NOTE:

- Students should note the dates of the examination carefully.
- The students should report in the examination hall 15 minutes before the start of examination.
- Mobile phones and other electronic gadgets are prohibited in the examination hall.
- Time slot of the practical examination will be declared by their concern HOD/Dean.**
- Change if any, in the time table shall be communicated on the university web site.



Controller of Examination

Copy to:

- Hon'ble Vice-Chancellor/ Pro Vice-Chancellor/OSD
- The Registrar

- All the Deans / HODs
- Notice Board